

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|----------|------------------|---------|------------------|
| L1 | 54 | (Path adj Overhead).clm. | US-PGPUB | OR | ON | 2007/06/20 12:41 |
| L2 | 18 | (((Path adj Overhead) or POH) and (Stor\$3 or record\$3 or read\$3 or writ\$3 or retriev\$3)).clm. | US-PGPUB | OR | ON | 2007/06/20 12:55 |
| L3 | 6 | (((Path adj Overhead) or POH) and (RAM or FIFO)).clm. | US-PGPUB | OR | ON | 2007/06/20 12:59 |
| L4 | 90 | (((Path adj Overhead) or POH)).clm. | US-PGPUB | OR | ON | 2007/06/20 12:59 |

Day :
 Wednesday
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 6/20/2007
 Time: 14:17:27


PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = WANG

First Name = JAMES

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|--------------------------|-------------------------|--------|------------|--|---------------|
| 06289895 | Not Issued | 161 | 08/04/1981 | SHIFTING DEVICE OF AN ELECTRIC PARKING LOT | WANG, JAMES |
| 06725109 | D296507 | 150 | 04/19/1985 | DINNER PLATE OR SIMILAR ARTICLE | WANG, JAMES |
| 06825887 | D298808 | 150 | 02/04/1986 | VOLTMETER OR SIMILAR ARTICLE | WANG, JAMES |
| 07231272 | Not Issued | 161 | 08/12/1988 | PROCESS FOR FABRICATING A REVERSIBLE SYNTHETIC LEATHER | WANG, JAMES |
| 07511509 | D330338 | 150 | 04/19/1990 | BATTERY TESTER | WANG, JAMES |
| 07892935 | Not Issued | 161 | 06/03/1992 | PROCESS FOR FABRICATING A REVERSIBLE SYNTHETIC LEATHER | WANG, JAMES |
| 08207705 | 5467294 | 150 | 03/09/1994 | HIGH SPEED, LOW POWER DIRECT DIGITAL SYNTHESIZER | WANG, JAMES |
| 08221840 | 5569971 | 150 | 03/31/1994 | READILY ASSEMBLED SPARK ELECTRODE | WANG, JAMES |
| 08237794 | Not Issued | 161 | 05/04/1994 | ANTI-FUNGAL AGENTS | WANG, JAMES |
| 08303824 | Not Issued | 161 | 09/09/1994 | WATER DISPERSIBLE ETHYLENE IONOMERS | WANG, JAMES |
| 08428974 | 5739104 | 250 | 04/26/1995 | ANTI-FUNGAL AGENTS | WANG, JAMES |
| 08473584 | Not Issued | 161 | 06/07/1995 | WATER DISPERSIBLE ETHYLENE IONOMERS | WANG, JAMES |
| 08474471 | Not Issued | 161 | 06/07/1995 | WATER DISPERSIBLE ETHYLENE IONOMERS | WANG, JAMES |
| 08478906 | Not Issued | 161 | 06/07/1995 | WATER DISPERSIBLE ETHYLENE IONOMERS | WANG, JAMES |

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|-----------------|------------|-----|------------|--|-------------|
| <u>08478907</u> | Not Issued | 161 | 06/07/1995 | WATER DISPERSIBLE ETHYLENE IONOMERS | WANG, JAMES |
| <u>08481866</u> | Not Issued | 161 | 06/07/1995 | WATER DISPERSIBLE ETHYLENE IONOMERS | WANG, JAMES |
| <u>08521117</u> | Not Issued | 161 | 08/29/1995 | SEALING MEANS FOR A VALVE | WANG, JAMES |
| <u>08627970</u> | Not Issued | 161 | 04/04/1996 | POWER INTERCEPTION CIRCUIT | WANG, JAMES |
| <u>08660701</u> | Not Issued | 161 | 06/05/1996 | PROCESS FOR FABRICATING A REVERSIBLE SYNTHETIC LEATHER | WANG, JAMES |
| <u>08689889</u> | 5790074 | 150 | 08/15/1996 | AUTOMATED LOCATION VERIFICATION AND AUTHORIZATION SYSTEM FOR ELECTRONIC DEVICES | WANG, JAMES |
| <u>08726520</u> | Not Issued | 161 | 10/07/1996 | CONNECTING MEANS OF A VALVE | WANG, JAMES |
| <u>08812855</u> | 6308062 | 150 | 03/06/1997 | WIRELESS TELEPHONY SYSTEM ENABLING ACCESS TO PC BASED FUNCTIONALITIES | WANG, JAMES |
| <u>08929900</u> | Not Issued | 169 | 09/15/1997 | SHIELD OR RING SURROUNDING SEMICONDUCTOR WORKPLACE IN PLASMA CHAMBER | WANG, JAMES |
| <u>08931864</u> | Not Issued | 164 | 09/16/1997 | SHIELD OR RING SURROUNDING SEMICONDUCTOR WORKPIECE IN PLASMA CHAMBER | WANG, JAMES |
| <u>09004472</u> | Not Issued | 161 | 01/08/1998 | SEALING GASKET MODULE FOR A VALVE | WANG, JAMES |
| <u>09086072</u> | 6166556 | 150 | 05/28/1998 | METHOD FOR TESTING A SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE TESTED THEREBY | WANG, JAMES |
| <u>09125014</u> | Not Issued | 161 | 05/03/1999 | PROCESSING PAPER FOR PRODUCING WET TYPE SYNTHETIC LEATHER AND METHOD OF ITS APPLICATION PROCESSING | WANG, JAMES |
| <u>09269631</u> | 6331604 | 150 | 03/31/1999 | GRAFTED RUBBER-LIKE SILICONE GEL WITH ENHANCED OIL COMPATIBILITY AND ITS | WANG, JAMES |

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|-----------------|----------------|-----|------------|---|-------------|
| | | | | SYNTHETIC PROCESS | |
| <u>09383362</u> | <u>6113046</u> | 150 | 08/26/1999 | ANGLE-ADJUSTABLE, AUTO-LOCKING APPARATUS SUPPORT | WANG, JAMES |
| <u>09507746</u> | <u>6615372</u> | 150 | 02/22/2000 | METHOD OF RETRIEVING AND DISPLAYING PROBLEMATIC INFORMATION ABOUT A PRINTER PRIOR TO A PRINT JOB BEING PRINTED ON THE PRINTER | WANG, JAMES |
| <u>09610526</u> | <u>6367756</u> | 150 | 07/07/2000 | Adjustable device support and anchor means arrangement | WANG, JAMES |
| <u>09665484</u> | <u>6284093</u> | 150 | 09/20/2000 | Shield or ring surrounding semiconductor workpiece in plasma chamber | WANG, JAMES |
| <u>09670413</u> | Not Issued | 161 | 09/26/2000 | Inductive write head having high magnetic moment poles and method for making same | WANG, JAMES |
| <u>09811474</u> | <u>6446667</u> | 150 | 03/20/2001 | BALL VALVE STRUCTURE | WANG, JAMES |
| <u>09823293</u> | Not Issued | 66 | 03/30/2001 | Onboard RAM based FIFO with pointers to buffer overhead bytes of synchronous payloads enevelopes in synchronous optical network | WANG, JAMES |
| <u>09875601</u> | <u>6870712</u> | 150 | 06/05/2001 | INDUCTIVE WRITER WITH FLAT TOP POLE AND PEDESTAL DEFINED ZERO THROAT | WANG, JAMES |
| <u>09912848</u> | Not Issued | 71 | 07/24/2001 | Method and system for implementing automatic bid status refresh and item attribute updates in an electronic exchange | WANG, JAMES |
| <u>09912891</u> | Not Issued | 61 | 07/24/2001 | Method and system for implementing catalog inventory auctions in an electronic exchange | WANG, JAMES |
| <u>09924671</u> | Not Issued | 61 | 08/07/2001 | Method and system for implementing automatic auction extensions and adjustable bid increments in an electronic exchange | WANG, JAMES |
| <u>09947194</u> | <u>6689249</u> | 150 | 09/04/2001 | SHIELD OR RING SURROUNDING SEMICONDUCTOR | WANG, JAMES |

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|--------------------------|-------------------------|-----|------------|--|-------------|
| | | | | WORKPIECE IN PLASMA CHAMBER | |
| 10205476 | Not Issued | 41 | 07/24/2002 | System and method for highly-scalable real-time and time-based data delivery using server clusters | WANG, JAMES |
| 10277463 | 6940688 | 150 | 10/21/2002 | COIL INDUCTIVE WRITER HAVING A LOW INDUCTANCE AND SHORT YOKE LENGTH | WANG, JAMES |
| 10305559 | 6873118 | 150 | 11/27/2002 | FIELD EMISSION CATHODE STRUCTURE USING PERFORATED GATE | WANG, JAMES |
| 10326541 | 6818265 | 150 | 12/23/2002 | METHOD OF FABRICATION REAL FLOWER ARTISTIC STICKER | WANG, JAMES |
| 10337778 | Not Issued | 161 | 01/08/2003 | Radio communication apparatus equipped with temperature control function | WANG, JAMES |
| 10714421 | 6937203 | 150 | 11/14/2003 | MULTI-BAND ANTENNA SYSTEM SUPPORTING MULTIPLE COMMUNICATION SERVICES | WANG, JAMES |
| 10792879 | Not Issued | 30 | 03/05/2004 | Wireless network structure | WANG, JAMES |
| 10814451 | 7046765 | 150 | 03/31/2004 | RADIOSURGERY X-RAY SYSTEM WITH COLLISION AVOIDANCE SUBSYSTEM | WANG, JAMES |
| 10820948 | Not Issued | 30 | 04/08/2004 | System and method for automatic linguistic indexing of images by a statistical modeling approach | WANG, JAMES |
| 10881315 | Not Issued | 41 | 06/30/2004 | Patient positioning assembly | WANG, JAMES |

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = NIGAM

First Name = ANURAG

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|-----------------|----------------|--------|------------|---|---------------|
| <u>08639555</u> | <u>5893929</u> | 150 | 04/29/1996 | MUTUALLY CONTROLLED MATCH-LINE-TO-WORD-LINE TRANSFER CIRCUIT | NIGAM, ANURAG |
| <u>08639556</u> | <u>5703803</u> | 150 | 04/29/1996 | DYNAMICALLY CONTROLLED, CROSS-STACKED CAM CELL | NIGAM, ANURAG |
| <u>08969011</u> | <u>6012133</u> | 150 | 11/12/1997 | MUTUALLY CONTROLLED MATCH-LINE-TO-WORD-LINE TRANSFER CIRCUIT | NIGAM, ANURAG |
| <u>09192975</u> | <u>6349092</u> | 150 | 11/16/1998 | BLSR NODE EXTENSION | NIGAM, ANURAG |
| <u>09751764</u> | <u>6993047</u> | 150 | 12/30/2000 | ANY SIZE AND LOCATION OF CONCATENATED PACKET DATA ACROSS SONET FRAMES IN A SONET SIGNAL | NIGAM, ANURAG |
| <u>09823293</u> | Not Issued | 66 | 03/30/2001 | Onboard RAM based FIFO with pointers to buffer overhead bytes of synchronous payloads enevelopes in synchronous optical network | NIGAM, ANURAG |
| <u>11035639</u> | Not Issued | 30 | 01/14/2005 | Any size and location of concatenated packet data across SONET frames in a SONET signal | NIGAM, ANURAG |
| <u>11036224</u> | Not Issued | 93 | 01/14/2005 | ANY SIZE AND LOCATION OF CONCATENATED PACKET DATA ACROSS SONET FRAMES IN A SONET SIGNAL | NIGAM, ANURAG |
| <u>11124458</u> | <u>7165200</u> | 150 | 05/05/2005 | SYSTEM AND METHOD FOR CHARACTERIZING A SIGNAL | NIGAM, ANURAG |

| | | | | | |
|-----------------|---------------|-----|------------|---|---------------|
| | | | | PATH USING A SUB-CHIP SAMPLER | |
| <u>60661078</u> | Not Issued | 159 | 03/10/2005 | Power amplifier with high linearity, optimum efficiency by using parallel biasing technique in power stage | NIGAM, ANURAG |

Inventor Search Completed: No Records to Display.

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Day :
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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = STILES

First Name = DAVID

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|-----------------|----------------|--------|------------|--|---------------|
| <u>08042298</u> | Not Issued | 162 | 04/02/1993 | METHOD AND APPARATUS FOR STORE-INTO-INSTRUCTION-STREAM DETECTION AND MAINTAINING BRANCH PREDICTION CACHE CONSISTENCY | STILES, DAVID |
| <u>08678870</u> | <u>5764556</u> | 250 | 07/12/1996 | METHOD AND APPARATUS FOR PERFORMING FLOATING POINT ADDITION | STILES, DAVID |
| <u>09751255</u> | <u>6765916</u> | 150 | 12/30/2000 | METHOD AND APPARATUS FOR PROCESSING OF MULTIPLE PROTOCOLS WITHIN DATA TRANSMISSION SIGNALS | STILES, DAVID |
| <u>09751764</u> | <u>6993047</u> | 150 | 12/30/2000 | ANY SIZE AND LOCATION OF CONCATENATED PACKET DATA ACROSS SONET FRAMES IN A SONET SIGNAL | STILES, DAVID |
| <u>09823480</u> | <u>6990121</u> | 150 | 03/30/2001 | METHOD AND APPARATUS FOR SWITCHING DATA OF DIFFERENT PROTOCOLS | STILES, DAVID |
| <u>09887299</u> | <u>7209436</u> | 150 | 06/22/2001 | METHOD AND APPARATUS FOR VARIABLE RATE PIPES | STILES, DAVID |
| <u>09887302</u> | Not Issued | 161 | 06/22/2001 | Method and apparatus for a non-BLSR protected layer 2/3 channel | STILES, DAVID |
| <u>09887303</u> | <u>7016300</u> | 150 | 06/22/2001 | PROTECTION MECHANISM FOR AN OPTICAL RING | STILES, DAVID |
| <u>09887957</u> | <u>6975589</u> | 150 | 06/22/2001 | METHOD AND APPARATUS FOR A HYBRID VARIABLE | STILES, DAVID |

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|-----------------|----------------|-----|------------|---|------------------|
| | | | | RATE PIPE | |
| <u>09997375</u> | <u>7050395</u> | 150 | 11/30/2001 | METHOD AND APPARATUS FOR DISABLING AN INTERFACE BETWEEN NETWORK ELEMENT DATA PROCESSING UNITS | STILES, DAVID |
| <u>10014726</u> | Not Issued | 161 | 12/10/2001 | Method and apparatus for using timeslots within a signal | STILES, DAVID |
| <u>10035560</u> | Not Issued | 161 | 10/23/2001 | Method and apparatus for combining 8b/10b encoding and error correction control | STILES, DAVID |
| <u>10109521</u> | Not Issued | 41 | 03/28/2002 | Method and apparatus for processing channelized and unchannelized data within a signal | STILES, DAVID |
| <u>10906733</u> | Not Issued | 30 | 03/03/2005 | DOOR FRAME SYSTEM UTILIZING HEADER ASSEMBLY WITH CONCEALED AUTOMATIC OPERATOR | STILES, DAVID |
| <u>11035639</u> | Not Issued | 30 | 01/14/2005 | Any size and location of concatenated packet data across SONET frames in a SONET signal | STILES, DAVID |
| <u>11036224</u> | Not Issued | 93 | 01/14/2005 | ANY SIZE AND LOCATION OF CONCATENATED PACKET DATA ACROSS SONET FRAMES IN A SONET SIGNAL | STILES, DAVID |
| <u>60001509</u> | Not Issued | 159 | 07/18/1995 | FLOATING POINT ADDER | STILES, DAVID |
| <u>60258760</u> | Not Issued | 159 | 12/30/2000 | Method and apparatus for switching data of different protocols | STILES, DAVID |
| <u>60258761</u> | Not Issued | 159 | 12/30/2000 | Method and apparatus for a non-BLSR protected layer 2/3 channel | STILES, DAVID |
| <u>60333845</u> | Not Issued | 159 | 11/28/2001 | Method and apparatus for processing channelized and unchannelized data within a signal | STILES, DAVID |
| <u>60550035</u> | Not Issued | 159 | 03/03/2004 | Door frame system utilizing header assembly with concealed automatic operator | STILES, DAVID |
| <u>08828527</u> | <u>5806594</u> | 150 | 03/31/1997 | COMPOSITIONS AND METHODS FOR CEMENTING | STILES, DAVID A. |

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|-----------------|----------------|-----|------------|---|------------------|
| | | | | A WELL | |
| <u>11625832</u> | Not Issued | 30 | 01/23/2007 | ENGINE PCV SYSTEM WITH VENTURI NOZZLE FOR FLOW REGULATION | STILES, DAVID K. |
| <u>11799326</u> | Not Issued | 17 | 05/01/2007 | Reverse braking system for rear wheel drive vehicles | STILES, DAVID K. |
| <u>60796599</u> | Not Issued | 159 | 05/01/2006 | Reverse braking system for rear wheel drive vehicles | STILES, DAVID K. |
| <u>07726564</u> | Not Issued | 166 | 07/08/1991 | UTILITY DELIVERY SYSTEM | STILES, DAVID K. |
| <u>08261047</u> | Not Issued | 161 | 06/16/1994 | UTILITY DELIVERY SYSTEM | STILES, DAVID K. |
| <u>08036155</u> | <u>5327329</u> | 250 | 03/24/1993 | LIGHTING ATTACHMENTS FOR IN-LINE ROLLER OR BLADE SKATES | STILES, DAVID L. |
| <u>29006291</u> | <u>D354787</u> | 150 | 03/24/1993 | LIGHTING ATTACHMENTS FOR SKATES | STILES, DAVID L. |
| <u>09547981</u> | <u>6499123</u> | 150 | 04/12/2000 | METHOD AND APPARATUS FOR DEBUGGING AN INTEGRATED CIRCUIT | STILES, DAVID R. |
| <u>09823293</u> | Not Issued | 66 | 03/30/2001 | Onboard RAM based FIFO with pointers to buffer overhead bytes of synchronous payloads enevelopes in synchronous optical network | STILES, DAVID R. |
| <u>09823871</u> | <u>7158540</u> | 150 | 03/30/2001 | RING NETWORK ELEMENT AND THE RING NETWORK ARCHITECTURES IT ENABLES | STILES, DAVID R. |
| <u>09837448</u> | <u>6959008</u> | 150 | 04/17/2001 | ALIGNMENT OF TDM-BASED SIGNALS FOR PACKET TRANSMISSION USING FRAMED AND UNFRAMED OPERATIONS | STILES, DAVID R. |
| <u>10113009</u> | Not Issued | 71 | 03/29/2002 | Updating provisioning data in a network element | STILES, DAVID R. |
| <u>11584360</u> | Not Issued | 25 | 10/20/2006 | Ring network element and the ring network architectures it enables | STILES, DAVID R. |
| <u>60280568</u> | Not Issued | 159 | 03/30/2001 | Method and apparatus for shadowing network element control cards | STILES, DAVID R. |
| <u>07315358</u> | Not Issued | 164 | 02/24/1989 | DISTRIBUTED PIPELINE CONTROL FOR A COMPUTER | STILES, DAVID R. |
| <u>07483223</u> | <u>5226126</u> | 150 | 02/21/1990 | PROSESSOR HAVING | STILES, DAVID R. |

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|-----------------|----------------|-----|------------|---|------------------|
| | | | | PLURALITY OF FUNCTIONAL UNITS FOR ORDERLY RETIRING OUTSTANDING OPERATIONS BASED UPON ITS ASSOCIATED TAGS | |
| <u>07485304</u> | <u>5230068</u> | 150 | 02/26/1990 | CACHE MEMORY SYSTEM FOR DYNAMICALLY ALTERING SINGLE CACHE MEMORY LINE AS EITHER BRANCH TARGET ENTRY OR PRE-FETCH INSTRUCTION QUEUE BASED UPON INSTRUCTION SEQUENCE | STILES, DAVID R. |
| <u>07485306</u> | Not Issued | 166 | 02/26/1990 | TWO-LEVEL BRANCH PREDICTION CACHE | STILES, DAVID R. |
| <u>07485307</u> | <u>5093778</u> | 150 | 02/26/1990 | INTEGRATED SINGLE STRUCTURE BRANCH PREDICTION CACHE | STILES, DAVID R. |
| <u>07485312</u> | <u>5226130</u> | 150 | 02/26/1990 | METHOD AND APPARATUS FOR STORE-INTO- INSTRUCTION-STREAM DETECTION AND MAINTAINING BRANCH PREDICTION CACHE CONSISTENCY | STILES, DAVID R. |
| <u>07594878</u> | Not Issued | 166 | 10/09/1990 | METHOD AND APPARATUS FOR PARALLEL PROCESSING OF INSTRUCTIONS WITH BRANCH PREDICTION LOOK- UP | STILES, DAVID R. |
| <u>07844995</u> | <u>5163140</u> | 150 | 03/02/1992 | TWO-LEVEL BRANCH PREDICTION CACHE | STILES, DAVID R. |
| <u>07954441</u> | <u>5327547</u> | 150 | 09/30/1992 | TWO-LEVEL BRANCH PREDICTION CACHE | STILES, DAVID R. |
| <u>08025439</u> | <u>5442757</u> | 150 | 03/03/1993 | COMPUTER PROCESSOR WITH DISTRIBUTED PIPELINE CONTROL THAT ALLOWS FUNCTIONAL UNITS TO COMPLETE OPERATIONS OUT OF ORDER WHILE MAINTAINING PRECISE INTERRUPTS | STILES, DAVID R. |
| <u>08068364</u> | Not Issued | 166 | 05/27/1993 | PROCESSOR WITH BRANCH PREDICTION AND MULTIPLE INSTRUCTION PREFETCH STORAGE | STILES, DAVID R. |
| <u>08143549</u> | <u>5513330</u> | 150 | 10/27/1993 | APPARATUS FOR | STILES, DAVID R. |

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|-----------------|----------------|-----|------------|--|------------------|
| | | | | SUPERSCALAR INSTRUCTION PRE-DECODING USING CACHED INSTRUCTION LENGHTS | |
| <u>08236714</u> | Not Issued | 166 | 04/29/1994 | SET-ASSOCIATIVE CACHE MEMORY UTILIZING A SINGLE BANK OF PHYSICAL MEMORY | STILES, DAVID R. |
| <u>08270855</u> | <u>5515518</u> | 150 | 07/05/1994 | TWO-LEVEL BRANCH PREDICTION CACHE | STILES, DAVID R. |

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